Form 1449 (Modified)	Atty. Docket No. . NVIDP325	Application No.: 09/965,952
Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Applicant: Michael Johnson Filing Date: 09/27/2001	Group Art Unit: 2152

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	No
ω	B	TW 447205B	7/21/2001	TW	H04L	29/06		
ω	C	TW 448407B	8/1/2001	TW	G06F	9/45		
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ω	E	Stevens, Richard W., "TCP/IP Illustrated Volume" Addison-Wesley Professional Computing Series, undated
ω	F	Abbot, Mark B., and Peterson, Larry L., "Increasing Network Throughput by Integrating Protocol Layers" IEEE 1993
ω	G	Wright, Maury, "Low-Cost Control LANs Add Automation to Homes, Autos, and Offices" EDN July 20, 1992
ω	H	Preston, David J., "Internet Protocols Migrate to Silicon For Networking Devices" Electronic Design April 14, 1997
ω	I	Muller, Raimund, "LON – das universelle Netzwerk" Electronik 22/1991
ω	J	Rang, Michael; Tantawy, Ahmed, "A Design Methodology for Protocol Processors" IEEE 1995
Examiner V. Vu		Date Considered 1-31-05

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w	D	Steenkiste, Peter, "A High-Speed Network Interface for Distributed-Memory Systems: Architecture and Applications" ACM Transactions on Computer Systems, Vol. 15, No. 1 February 1997
w	E	Doumenis, Gr.A., Konstantoulakis, G.E., Reisis, D.I.and Stassinopoulos, G.I. "A Personal Computer Hosted Terminal Adapter For The Broadband Integrated Services Digital Network and Applications" National Technical University of Athens, Greece, undated
w	F	Womack, Lucas; Mraz, Ronald; Mendelson, Abraham, " A Study of Virtual Memory MTU Reassembly withing the PowerPC Architecture" IEEE 1997
w	G	Steenkiste, Peter, "A Systematic Approach to Host Interface Design for High-Speed Networks" IEEE March 1994
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ᴎ	C	Yocum, Kenneth G.; Chase, Jeffrey S.; Gallatin, Andrew J.; Lebeck, Alvin R., "Cut-Through Delivery in Trapeze: An Exercise in Low-Latency Messaging IEEE 1997
✓ʋ	D	Varada, S.; Yang, Y.; Evans, D., "Data and Buffer Management in ATM Systems" TranSwitch Corporation , undated
v✓	E	Bonjour, Dominique; de Hauteclocque, Gaelle; le Moal, Jacques, "Design and Application of ATM LAN/WAN Adapters" IEEE 1998
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✔	D	Nagata, Takahiko; Hosoda, Yamashita, Hiroyuki, "High-Performance TCP/IP/ATM Communication Board" NTT Information and Communication Systems Laboratories 1998
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✔✔	F	Jolitz, William Frederick, "High-Speed Networking: Header prediction and forward-error correction for very high-speed data transfer" Dr. Dobbs Journal, August 1992
✔✔	G	Chiswell, Dave "Implementation Challenges for 155Mbit ATM Adapters" ISBN# 0-7803-2636-9 , undated
✔✔	H	Wright, Maury "Intelligent Ethernet Boards" EDN June 23, 1988
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ᴎ	D	Korablum, Deborah F., "Protocol Implementation and Other Performance Issues for Local and Metropolitan Area Networks" EEE 1988
✓✓	E	Dittia, Zubin D.; Parulkar, Guru M.; Jr., Jerome R. Cox, "The APIC Approach to High Performance Network Interface Design: Protect4ed DMA and Other Techniques" IEEE 1997
✓✓	F	Rutsche, Erich, "The Architecture of a Gb/s Multimedia Protocol Adapter" ACM SIGCOMM Computer Communication Review, *undated*
✓✓	G	Moldeklev, Kjersti; Klovning, Espen; Kure, Oivind, "The effect of end system hardware and software on TCP/IP throughput performance over a local ATM Network", *undated*
✓✓	H	Kanakia, Hemant; Cheriton, David R., "The VMP Network Adapter Board (NAB) High Performance Network Communication for Multiprocessors" ACM 1988

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✓✓	D	Nielson, Dr. Michael J.K., "TURBOchannel" IEEE 1991
✓v	E	New Media News, www. newmedianews.com/02197/ts_inettuner.html , *undated* ✓
vv	F	Kelly, T. "Cheap Internet Hardware that Fits in Everything" ZDNet, www.zdnet.co.uk/news/1998/77/ns-5998.html
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v✓		"Storage Networking Industry Association" iSCSI Building Blocks for IP Storage Networking, www.ipstorage.org, *undated*
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